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| | Filing Date | | 2009-06-22 | |
| | First Named Inventor | Furrer | | |
| | Art Unit | 2416 | | |
| | Examiner Name | | | |
| | Attorney Docket Number | CH920030062US1 | | |

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| /J.B./ | 1 | 6512798 | | 2003-01-28 | Akiyama | |

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| /J.B./ | 1 | 0668678 | EP | A2 | 1995-08-23 | Cariolaro | | <input type="checkbox"/> |
| /J.B./ | 2 | 2001308815 | JP | | 2001-11-02 | Kaneko | Abstract | <input checked="" type="checkbox"/> |
| /J.B./ | 3 | 7226724 | JP | | 1995-08-22 | Seki | Abstract | <input checked="" type="checkbox"/> |

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| 1 | 11163953 | JP | | 1999-06-18 | Iwaki | Abstract | <input checked="" type="checkbox"/> |
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| /J.B./ | 1 | MATUSIAK, "Implementing Fast Fourier Transform Algorithms of Real-Valued Sequences with the PTS320 DSP Family", Texas Instruments Application Report SPRA291, Dec. 1997, pp 12-19, 30-40, http://www.eetkorea.com/Articles/2001May/2001May07_DSP_EMS_AN.PDF . | <input type="checkbox"/> |
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